

Release Notes  
ATLAS Compiler  
ATLAS Release 3.24.18 (20090825)  
25 August 2009

## 1.0 Overview

The primary purpose for this release is to include fixes for some Problem Reports. The following describes an overview of changes included within version 20090825 (3.24.18) of the following 1985 ATLAS based compiler(s).

CASS / PC

and version 20090825 (3.24.18) of the following ATLAS support tools:

Switch / Ita Compiler

## 1.1 Enhancements

None

## 1.2 Problem Reports

09-062 and 09-068.

## 2.0 Detailed Description

### 2.1 Problem Reports

#### 2.1.1 09-062 Problem processing APPLY, SHORT HI <pin1> LO <Pin2>

1) In order to keep backwards compatibility and get the compiler to do what you expect it to do for APPLY SHORT HI LO, please use the "-l" option ("l" as in litre, lamp,...) at compile time.

2) In the sgl file, you will see pseudo UUTPins \_HI\_ and \_LO\_ in place of HI and LO.

This is normal.

The released methodology of handling APPLY, SHORT in the ATLAS compiler maps

APPLY, SHORT, CNX HI J1 LO J9 to

APPLY, SHORT, CNX FROM J1 TO J9

which will Allocate correctly using a conventional PAWS .ITA file but could not have been made to function using a CASS .LU file because there is not a way of describing a UUTPin to UUTPin path (As far as I know.)

This compiler maps

APPLY, SHORT, CNX HI J1 LO J9 to

APPLY, SHORT, CNX FROM \_HI\_ \_LO\_ TO J1 J9

which the allocator treats as requiring paths from \_HI\_ to J1 and from \_LO\_ to J9. This will allocate correctly using a .LU file if there are entry's for SHORTHIJ1 and SHORTLOJ9.

The UUTPins, which we refer to as pseudo UUTPins, which the ATLAS compiler inserts are generated by the Switch Compiler automatically from the .LU entries.

### 2.1.2 09-068 2 and 4 wire measurements for the DMM (impedance)

The rule will be:

1) Sensor Statement

2) Noun = IMPEDANCE

3) CNX field is: CNX HI <UUTPinA> <UUTPinB> LO <UUTPinC> <UUTPinD>

Translate CNX field to CNX HI <UUTPinB> SENSE-HI <UUTPinB> LO <UUTPinC> SENSE-LO <UUTPinB>

If the verb action includes a SETUP then a SET 4WIR ON will be generated.

If the CNX field includes ony single <UUTPins> for each port and the verb action includes a SETUP then a SET 4WIR OFF will be generated.

As the same resource is to be used for both environments, we recommend that you add four-wire and two-wire control fields for the same FNC#.

At run time, look for the associated modifier token to determine if you need to configure it in a 2 or 4 wire setting.

4-wire should be a direct result of having HI pin1 pin2 LO pin3 pin4 where any of the pins can be something or NONE.

2-wire will be triggered by HI pin1 LO pin2.